





IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2811 Examiner: To be assigned

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Applicant(s):		An-Ru Andrew CHENG et al.)		
Serial No. :		10/626,634))		
Filed	:	July 25, 2003)) >		
For :		Integrated circuit capable of locating failure process layers))	STATUS REQUEST	
Attorney Ref	:	TOP 298))		

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Please let us know the status of the above-identified application and when an Action may be expected. If the response to this status request is made by telephone, please ask for Tonya Harris (Docket Clerk) at extension 518.

Respectfully submitted,

February 8, 2005

Date

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SMR:dj

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